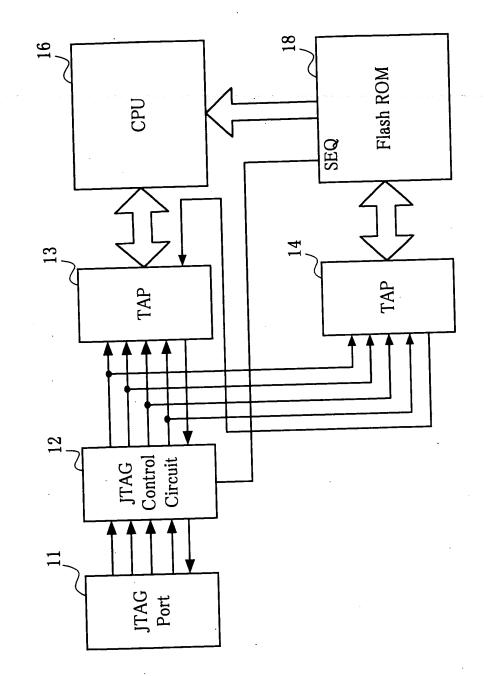
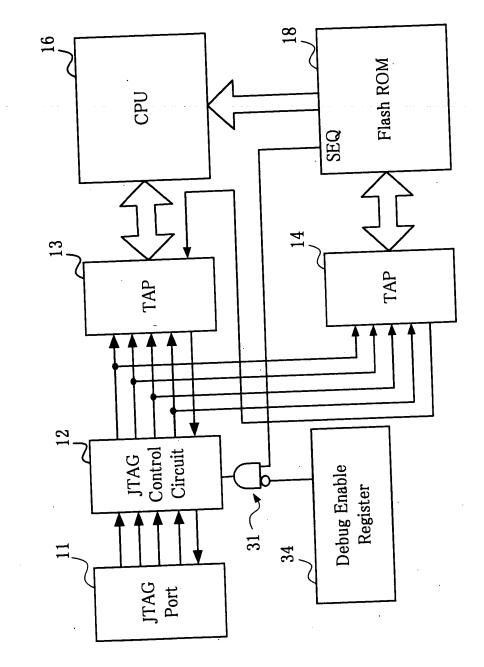


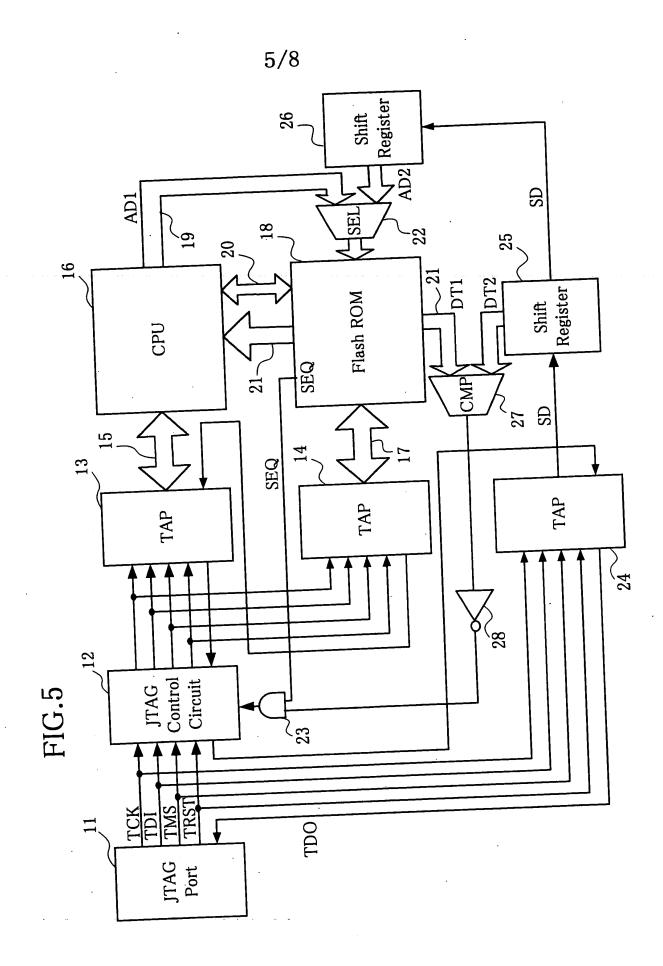
FIG.2

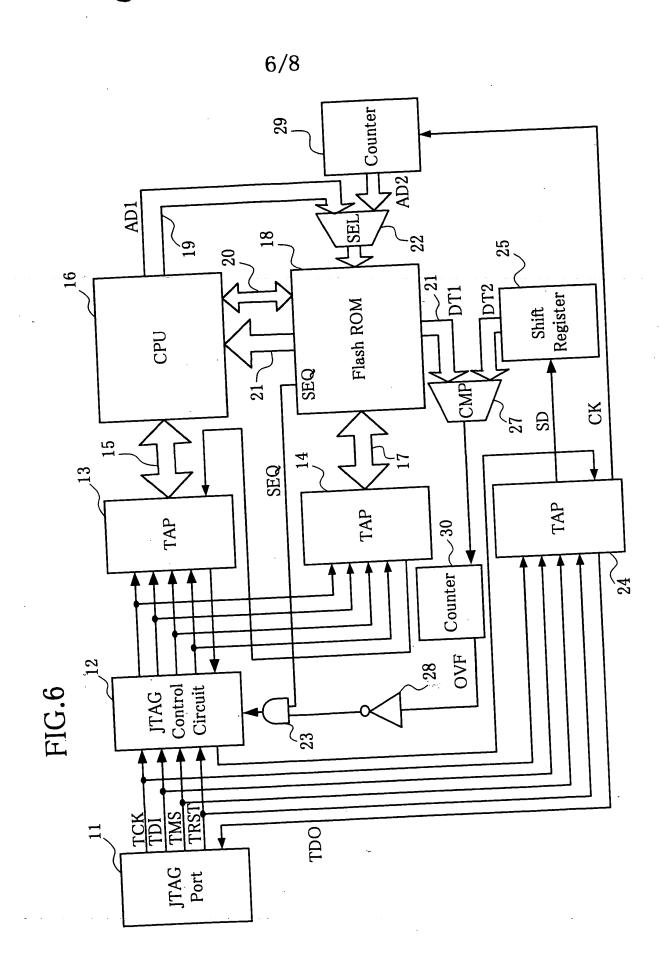


Flash ROM CPU SEQ TAP TAP12 JTAG Control Circuit Pin Scramble-Circuit FIG.3 JTAG Port 32 General-Purpose Port

FIG.4







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